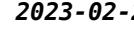


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this book provides a comprehensive survey of recent progress in the design and implementation of networks on chip it addresses a wide spectrum of on chip communication problems ranging from physical network to application layers specific topics that are explored in detail include packet routing resource arbitration error control correction application mapping and communication scheduling additionally a novel bi directional communication channel noc binoc architecture is described with detailed explanation written for practicing engineers in need of practical knowledge about the design and implementation of networks on chip includes tutorial like details to introduce readers to a diverse range of noc designs as well as in depth analysis for designers with noc experience to explore advanced issues describes a variety of on chip communication architectures including a novel bi directional communication channel noc from the foreword overall this book shows important advances over the state of the art that will affect future system design as well as r d in tools and methods for noc design it represents an important reference point for both designers and electronic design automation researchers and developers giovanni de micheli  nothing is simple in fifteen year old casey s life she has two families and two houses and two rooms of her own sometimes this makes her feel lucky but other times she feels practically homeless she also has a brother with asperger s he s interesting to have around and she loves him very much but he s also complicated and speaking of complicated casey discovers she s a time traveler she ll be doing her algebra homework or on her way to work at the restaurant when suddenly she s in some other time and place in some other body and she s supposed to do something help somebody fix some problem but how     

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