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AIMED AT THE STUDENT WHO WISHES TO LEARN PRINCIPLES OF DIGITAL CIRCUITS AND THEN APPLY THEM TO DESIGNS THIS TEXT INCLUDES PIN OUTS FOR MORE THAN 60 DIGITAL IC CHIPS THE USE OF STANDARD LOGIC SYMBOLS ALONG WITH IEEE STANDARD LOGIC AND A REVIEW OF IEEE SYMBOLS IN THE APPENDIX EMPHASIS IS GIVEN TO TWO DIGITAL INTEGRATED CIRCUIT FAMILIES TRANSISTOR TRANSISTOR LOGIC TTL AND COMPLEMENTARY METAL OXIDE SILICON CMOS LOGIC

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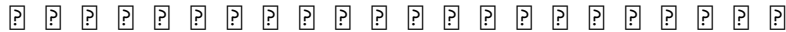
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EVENT DRIVEN LOGIC CIRCUITS USING THE NAND SEQUENTIAL EQUATION THE FINAL CHAPTER DEALS WITH SIMPLE CODING TECHNIQUES AND THE PRINCIPLES OF ERROR DETECTION AND CORRECTION THIS BOOK IS A VALUABLE RESOURCE FOR UNDERGRADUATE STUDENTS DIGITAL ENGINEERS AND SCIENTISTS

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